

PATENT APPLICATION

Sheet 1 of 2

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	200312082-1	10/748967	
	APPLICANT		
	Yong Wang et al.		
	FILING DATE	GROUP	
	12/30/03	2125	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
AP	1A	6,151,568	11-21-2000	Allen et al.	
AP	1B	2002/0112212	08-15-2002	Cohn et al.	
AP	1C	6,440,770	08-27-2002	Banerjee et al.	
AP	1D	2003/0001284	01-02-2003	List et al.	
AP	1E	6,534,723	03-18-2003	Asai et al.	
AP	1F	6,555,921	04-29-2003	Kwon et al.	
AP	1G	2003/0088395	05-08-2003	Frech et al.	
	1H				
	1I				
	1J				
	1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

AP	1Q	Joseph N. Kozhaya et al.; "A Multigrid-like Technique for Power Grid Analysis"; Proceedings of the 2001 IEEE/ACM International Conference on CAD, San Jose, CA, November 4-8, 2001; pages 1-13.
AP	1R	Srinivas Bodapati et al.; "High-Level Current Macro-Model For Power-Grid Analysis"; Annual ACM IEEE Design Automation Conference; New Orleans, LA, June 10-14, 2002; 6 pages.
AP	1S	Yu-Min Lee et al.; "Power Grid Transient Simulation in Linear Time Based on Transmission-Line-Modeling Alternating-Direction-Implicit Method"; IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 21, No. 11, November 2002; pages 1343-1352.

EXAMINER	/Albert Paladini/	DATE CONSIDERED	09/12/2006
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PATENT APPLICATION

Sheet 2 of 2

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 200312082-1	APPLICATION NO. 10/748,964	CONFIRMATION NO.
	APPLICANT Yong Wang et al.		
	FILING DATE 12/30/03	GROUP 2125	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A				
	1B				
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	1L					
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

AP	1Q	Yong Wang et al.; "Integrated Power Grid Modeling and Analysis"; 12th Topical Meeting on Electrical Performance of Electronic Packaging, Princeton, NJ, October 27-29, 2003; pages 1-4.
	1R	
	1S	

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